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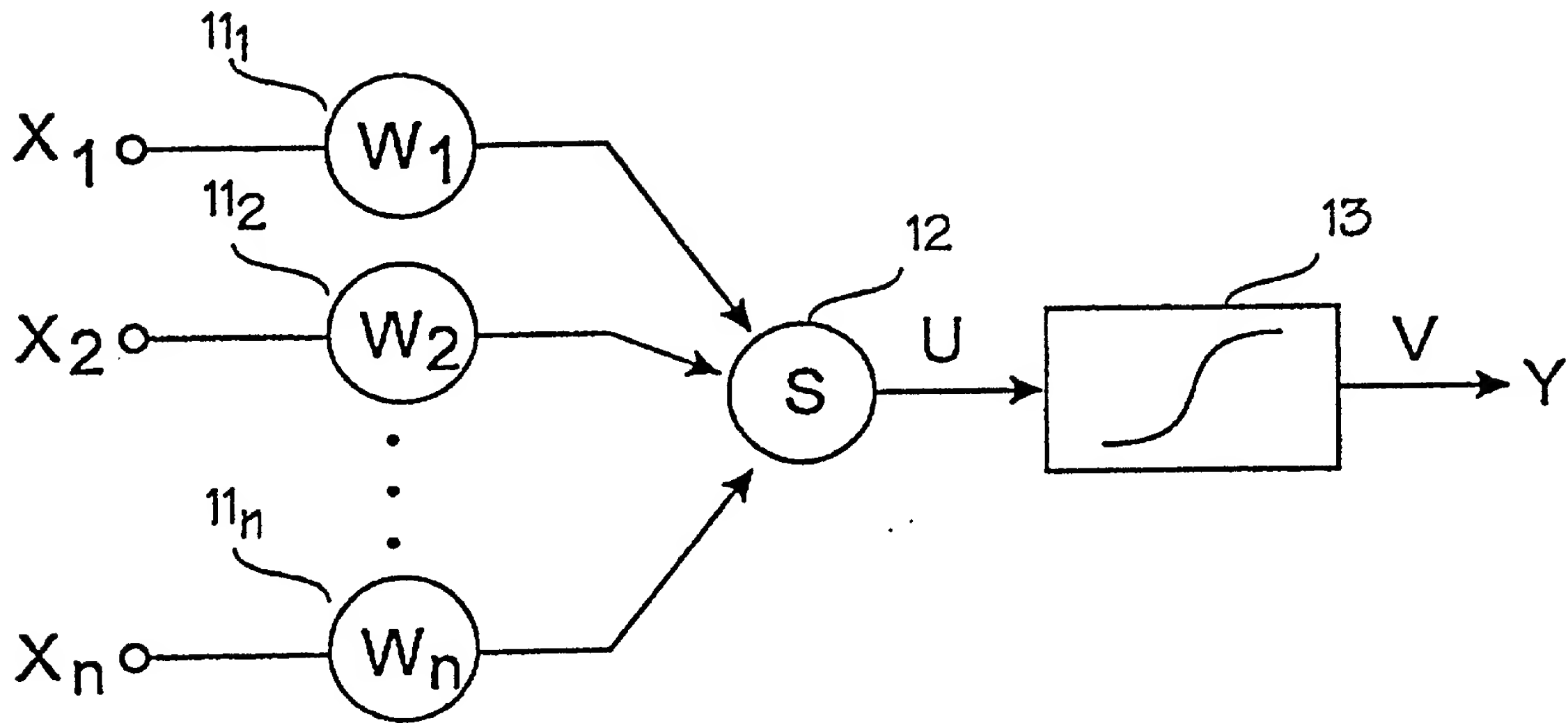


Fig. 1

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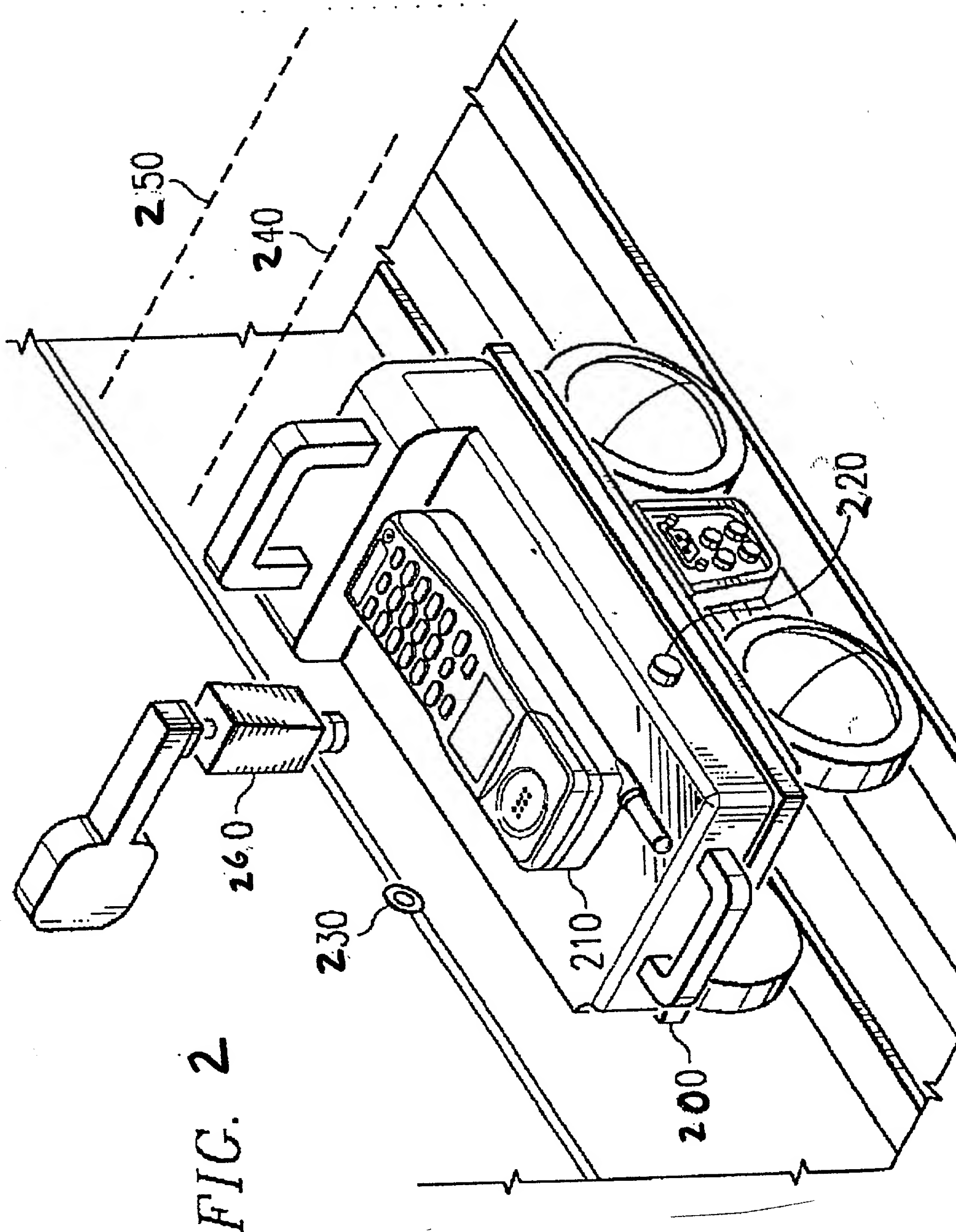


FIG. 2

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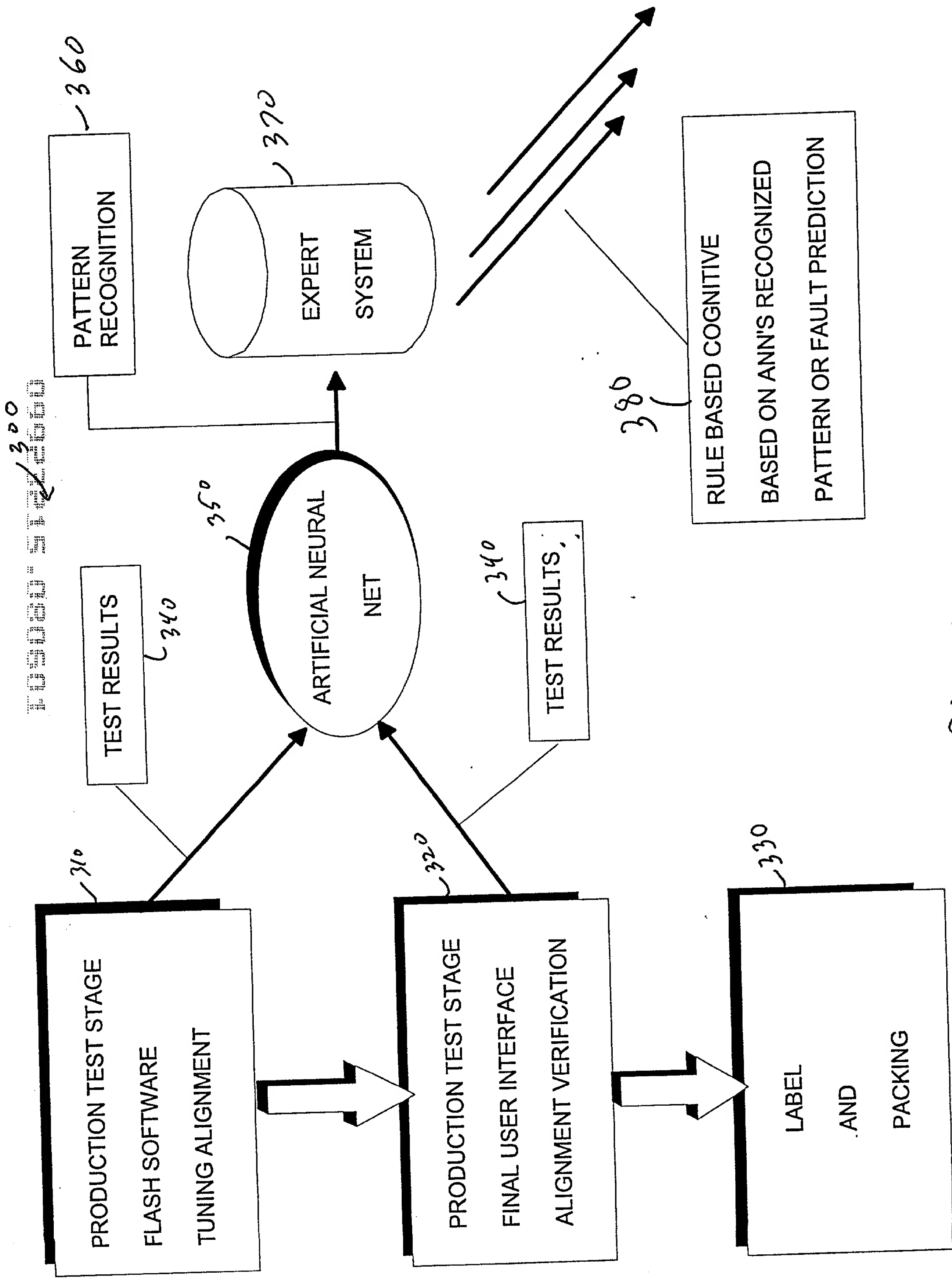


Fig. 3

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Line	JHD972 Line U	Stage	TUFINU	Re-load	Print	Options	Pareto	AIE's	Yield	Close					
ATK Name	4	5	6	7	8	9	10	11	12	13	14	15	FP Total	FP Total	FP Yield
TUFINU01	0	0	2	1	0	3	2	0	0	1	1		147	10	93.63%
TUFINU02	0	0	2	0	1	2	0	0	1	2	1		117	9	92.86%
TUFINU03	1	1	2	1	1	1	1	5	0	3	1		323	17	95.00%
TUFINU04	1	3	1	2	2	3	0	0	0	0	1		317	13	96.06%
TUFINU05	0	0	1	0	4	1	3	0	2	1			272	12	95.77%
TUFINU06	1	2	1	1	0	1	1	0	1	0	2		190	10	95.00%
TUFINU07	0	1	0	0	1	3	0	2	1	0			171	8	95.53%
TUFINU08	0	2	5	0	2	3	0	0	1	1	1		321	15	95.54%
TUFINU09	1	2	3	1	0	0	5	1	0	1	0		297	14	95.50%
TUFINU10	0	1	1	0	0	5	3	1	2	0	3		286	16	94.70%
Total 1st Pass	201	215	246	158	268	253	205	186	280	246	183	0	2441		
Total 1st Fail	4	12	18	6	11	22	15	9	8	9	10	0		124	
Hourly Yield %	98%	95%	93%	96%	96%	92%	93%	95%	97%	96%	95%	0%			95.17%

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Fig. 4

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Test	Test Description	FINU01	FINU02	FINU03	FINU04	FINU05	FINU06	FINU07	FINU08	FINU09	FINU10	Check
217	TXA SINAD	0.52			0.88	0.51	0.40		0.22	0.50	0.76	
267	Antenna Check (LB)	0.52										
215	TXA Power PL2	0.52			0.44	0.26	0.40	1.93	0.44	1.49	1.52	
241	MIC -> XEAR Loop te	0.52		0.46								
242	XMIC -> EAR Loop te	0.52		0.46	0.44	0.51	1.20		0.44	0.25	0.51	
257	Display Pattern A C	0.52						0.48	0.22			
230	RXD MAHO BER	1.04	1.43	0.91	0.88	1.28	1.20		0.67	0.50	1.26	
200	Serv test (DCCH800	1.55	2.14	0.68	0.44		0.40	0.48	0.67	0.74		
226	TXD ACP Alt Hi		0.71				0.40		0.22			
221	TXD Phase Error		0.71									
251	Keypad Functionalit		0.71	1.37						0.25		
255	Display Pattern B C		0.71	0.23	0.22			0.48				
204	TXD Phase Error			0.23	0.22	0.51			0.44	0.25	0.51	
213	RXD MAHO BER			0.46						0.25		
243	Head -> Ear Loop Te				0.22				0.67			
500	Wait for service BS				0.44	0.26	0.40				0.25	
202	TXD Power PL2					0.26						
218	Handoff (AVCH800->D					0.26						
253	Keypad Backlight Ch					0.26						
268	Mean of fctate mtrva					0.26						

Print

Close

Fig. 5.

